RIDA ZAINAB

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Current Address: Department of Electrical Engineering, CEET, University of the Punjab, Lahore.

Objective

To work in a challenging environment and grow as a professional.

Education

1	**M.Sc. (Electrical Engineering)**	University of Engineering & Technology (**UET**), Lahore, Pakistan
2	**B.Sc.** (**Electrical Engineering**)	University of Engineering & Technology (**UET**), Lahore, Pakistan

Projects

BSc. Final Year Project: "Biometrics System for Security Enhancement Applications"

A *stand-alone* robust security system is designed and implemented using Digital Image Processing techniques in Open

⊕ Computer Vision. Includes *face recognition*, *finger prints recognition* and *iris recognition* mechanisms.

⊕ **Characters display on alphanumeric LCD using 8051 uController**

⊕ Sound Amplifier Implementation and Comparison of different amplification techniques

⊕ Implementation of Database for CD-Shop using C language

⊕ Analog Controlled Variable Power Supply (0-12V)

Research Work

 MS thesis on "Transmission Loss reduction using optimal power flow based on chaotic firefly algorithm"

Technical Skills

⊕ *Microsoft Windows xx*, **Windows 7+; Microsoft Office, C/assembly** languages

⊕ Programming of uController**805x series**, **PIC uController**

⊕ **Proteus** (for Circuit Designing), and **Proteus-Ares** (for PCB Designing)

⊕ **MATLAB** Simulation and Calculations, Working on **R2009b**

⊕ **Verilog** and **Xilinx** programming and Simulation

⊕ **MATLAB** *Simulink*

Work Experience

Worked as an Internee engineer at DESCON, Electrical Power design Department.

Worked as a lecturer at department of Electrical Engineering, The Univesrity of Lahore.